

## CAMAC Equipment

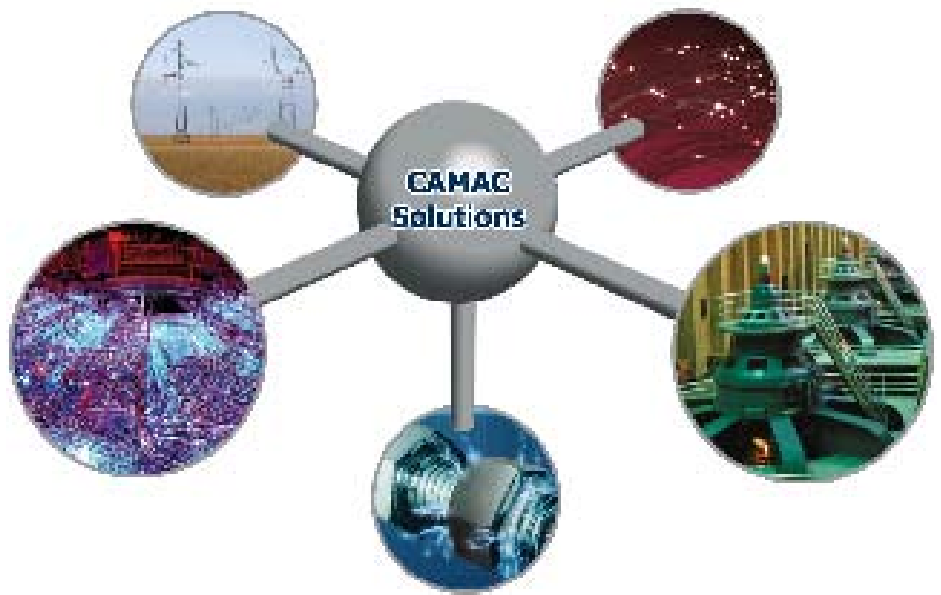
CAMAC, Computer Automated Measurement And Control, is an IEEE-standard (583), modular, high-performance, realtime data acquisition and control system concept.

Since 1969, CAMAC has been used in many thousands of scientific, industrial, aerospace, and defense test systems around the world.

### APPLICATIONS

Event counting  
Nuclear counting  
Frequency measurement  
Totalizing

## 3615 6-channel, 100 MHZ Counter



The Model 3615 is a single-width CAMAC module containing six independent 24-bit counters.

### FEATURES

- Six counters
- Maximum count 16,777,215
- LAM on overflow
- NIM standard inputs
- dc to 100 megahertz
- Ten nanosecond paired pulse resolution



**GENERAL DESCRIPTION**

The Model 3615 is a single-width CAMAC module containing six independent 24-bit counters. These counters accept terminated NIM\* signals at rates from dc to 100 megahertz (minimum pulse width three nanoseconds). Each counter has an overflow bit which is set on a carry from either Bit 16 or Bit 24 and generates a LAM, if enabled. The pattern of overflow bits in a module can be read. An overflow output for each counter can be wired to a rear-panel P.C. edge connector for cascading counters. Counters can be inhibited by Dataway Inhibitor by a front-panel signal. They can be individually cleared by Dataway command or as a group by Dataway clear or front-panel signal. The input is protected for a 50 volt transient or 4 volts dc. Counter inputs are terminated in 50 ohms while inhibit and clear inputs are bridged high impedance.

When precise timing of sequenced events is required, the Model 3655 Timing module provides a time base for the 3615 by asserting the Dataway Inhibit line for a programmable interval. A front-panel switch enables the inhibiting of counting by the Dataway Inhibit line.

When the count in any counter reaches 224 (or 216 selected by a jumper), the counter rolls over and continues to count, and a corresponding overflow LAM status bit is set. The six LAM status bits are OR'ed and, when enabled, produce a LAM request. The pattern of the six LAM status bits can be read to locate the specific counter that overflowed.

\*The nominal NIM signal is: -16 mA into 50 for a 1, and 0 mA for a 0.

**POWER REQUIREMENTS**

+6 Volts — 1200 mA  
-6 volts — 740 mA

**WEIGHT**

.62 kg. (1 lb. 6 oz.)

**ACCESSORIES**

Models 5910-Z1A, 5960-Z1A or 5960-Z1B                      Mating Connectors  
Models 5857-Axyz and 5857-Bxyz                                I/O Cables

**ORDERING INFORMATION**

MODEL	DESCRIPTION
3615-L2A	6-channel, 100 MHZ Counter

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